Fall 9-1-2005

EET 227T.01: Computer Fundamentals

Steven L. Stiff
University of Montana - Missoula, steven.stiff@umontana.edu

Let us know how access to this document benefits you.
Follow this and additional works at: https://scholarworks.umt.edu/syllabi

Recommended Citation
https://scholarworks.umt.edu/syllabi/9989

This Syllabus is brought to you for free and open access by the Course Syllabi at ScholarWorks at University of Montana. It has been accepted for inclusion in Syllabi by an authorized administrator of ScholarWorks at University of Montana. For more information, please contact scholarworks@mso.umt.edu.
EET227T COMPUTER FUNDAMENTALS
SYLLABUS

COURSE DESCRIPTION, POLICIES, AND MATERIALS

CREDITS: 4

PREREQUISITES: EET103T

INSTRUCTOR: Steven (Steve) L. Stiff
Phone: 243-7672
E-mail: steven.stiff@umontana.edu
Office: to be determined
Office Hours: to be determined

CLASS SCHEDULE:
Monday through Friday, 8:10am – 10:00am.
Class time will be approximately 50% lecture and 50% lab activity.

COURSE DESCRIPTION:
Covers the operation, application, and troubleshooting of TTL and CMOS logic devices, their use in combinatorial and sequential logic circuits, the interface between logic families, and the interface between digital and analog circuits. Also provides a study of Boolean algebra, binary and hexadecimal number systems, binary codes, and the analysis of the basic components and circuits used in semiconductor switching.

TEXT(S):
• Handouts, worksheets, and labs as provided by the instructor.

COURSE OBJECTIVES:
At the completion of this course, the student should be able to:
1) Identify the two basic types of electrical signals.
2) Convert numbers between decimal, binary, octal, and hexadecimal number systems.
3) Identify commonly used binary codes.
4) Name the major components used in implementing digital circuits and explain how they operate.
5) Explain the operation of digital logic gates.
6) Name the most frequently used combinatorial logic circuits and explain their operation.
7) Identify commonly used integrated circuit families used in digital equipment.
8) Use Boolean algebra express logic operations as equations and use Karnaugh maps to minimize (simplify) Boolean equations.
9) Identify and explain the operations of various types of flip-flops.
10) Describe the operation of binary and BCD counters, shift registers, and other sequential logic circuits.
11) Design both combinatorial and sequential logic circuits for a given application.
12) Name and describe the function of various data conversion devices, such as DACs, ADCs, and multiplexers.
13) Troubleshoot digital circuits using standard test equipment and specialized instruments.
STUDENTS WITH DISABILITIES:
- Eligible students with disabilities will receive appropriate accommodations in this course when requested in a timely way. Please speak with me after class or in my office. Please be prepared to provide a letter from your Disability Services for Students (DSS) Coordinator.
- For students planning to request testing accommodations, be sure to bring the form to me in advance of the two-day deadline for scheduling in the Academic Support Center (ASC).

GRADING:
- Points are awarded for each graded item. Your final grade is calculated by summing all points for a given category and weighted by its associated percentage as shown below.
- Letter grades are assigned as a percentage of weighted total points.

<table>
<thead>
<tr>
<th>Category</th>
<th>Percentage</th>
</tr>
</thead>
<tbody>
<tr>
<td>Homework, Quizzes, Attendance</td>
<td>35.0%</td>
</tr>
<tr>
<td>Labs (construction &amp; write-ups)</td>
<td>30.0%</td>
</tr>
<tr>
<td>Exams (including Final)</td>
<td>35.0%</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td><strong>100.0%</strong></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Letter Grade</th>
<th>Percentage</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>93.0% and up</td>
</tr>
<tr>
<td>B</td>
<td>85.0% - 92.0%</td>
</tr>
<tr>
<td>C</td>
<td>70.0% - 84.0%</td>
</tr>
<tr>
<td>D</td>
<td>60.0% - 69.0%</td>
</tr>
<tr>
<td>F</td>
<td>Below 60.0%</td>
</tr>
</tbody>
</table>

LAB & HOMEWORK MATERIALS (SUPPLIED BY STUDENT):
- EET227T parts kit (UMCOT bookstore)
- Breadboard (prototyping board)
- Graph paper (Engineer’s Computation Pad, Ampad #22-144)
- Additional materials as required by the instructor

EQUIPMENT (SUPPLIED BY UMCOT):
- Personal computer
- Oscilloscope
- Signal generator
- Variable voltage/current power supply
- Additional materials as provided by the instructor